

06.09.04

IFW

TRANSMITTAL OF FORMAL DRAWINGS

Docket No.
872.0178.U1(US)

Application Of: Hooli et al.

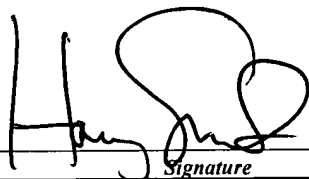
Serial No.	Filing Date	Confirmation No.	Examiner	Art Unit
10/783,049	February 19, 2004		Not yet assigned	2631

Invention: Chip-Level or Symbol-Level Equalizer Structure for Multiple Transmit and Receiver Antenna Configurations

Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Transmitted herewith are:

3 sheets of formal drawing(s) for this application.

☐ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).
Signature

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Dated:

6/7/2004

I certify that this document and attached formal drawings are being deposited on 6/7/04 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



Signature of Person Mailing Correspondence

Ann Okrentowich

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